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Silicon Y-bifurcated microchannels etched in 25 wt % TMAH water solution

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Abstract

In this study, Y-bifurcated microchannels fabricated from a {100} silicon in 25 wt % TMAH water solution at the temperature of 80 °C have been presented and analysed. We studied the etching of acute angles with sides along the <n10> crystallographic directions in the masking layer where 1 < n < 8. We considered symmetrical acute corners in the masking layer with respect to the <100> crystallographic directions. The angles between the appropriate <n10> and <100> crystallographic directions were smaller than 45 °. Moreover, we observed asymmetrical acute corners formed by the <n10> and <m10> crystallographic directions, where m≠n. We found that the obtained convex corners were not distorted during etching. Consequently, it is not necessary to apply convex corner compensation. These fabricated undistorted convex corners represent the angles of the bifurcations. The sidewalls of the microchannels are defined by etched planes of the {n11} and {100} families. Analytical relations were derived for the widths of the microchannels. The results enable simple and cost-effective fabrication of various complex silicon microfluidic platforms.

Keywords: Y bifurcation, microchannels, silicon, wet etching, TMAH

1. Introduction

Many studies have explored micromachining of (100) silicon substrates using anisotropic wet etching in 25 wt % tetramethylammonium hydroxide (TMAH) water solution [1-28]. The etch rates of crystallographic planes [1–13,15,26], roughness of silicon surface after etching [2,7,13,25], and problems of convex corner etching and their solutions [14–28] have been investigated. Pattern design in the masking layer and solution anisotropy determine the etched three-dimensional (3D) silicon shape. The distortion of convex corners during etching limits the production methods of complicated 3D silicon shapes [2,14,22,26]. The applied convex corner compensation depends on the design of the microdevice. Most of the designs are developed for the

patterns with sides along the <110> crystallographic directions [15–24]. Some of them cause unwanted residues at the bottom of the silicon structure at the end of etching [19,22]. These residues can influence the functioning of microdevices. Moreover, the sides of the pattern designs are only along the most common crystallographic directions <110> and <100>.

In our previous work [26–28], we presented a comprehensive study on the etching of patterns with sides along the <n10> crystallographic directions. In these papers, we presented etching of the (100) silicon in 25 wt % TMAH water solution at the temperature of 80 °C, using silicon dioxide as a masking layer. We explored the etching of mask patterns in the shapes of squares and parallelograms in the corresponding studies. In Ref.[26], we studied square islands

and apertures with sides along the <n10> crystallographic directions, where $0 \le n < 10$. In [27–28], we analysed islands in the shapes of parallelograms with sides along predetermined crystallographic directions <n10>(1 < n < 8) and <100>. During the etching of patterns presented in

Ref.[28], sustainable types of 3D silicon shapes were obtained. It was concluded that the convex corners were not distorted, although, no convex corner compensation was applied.

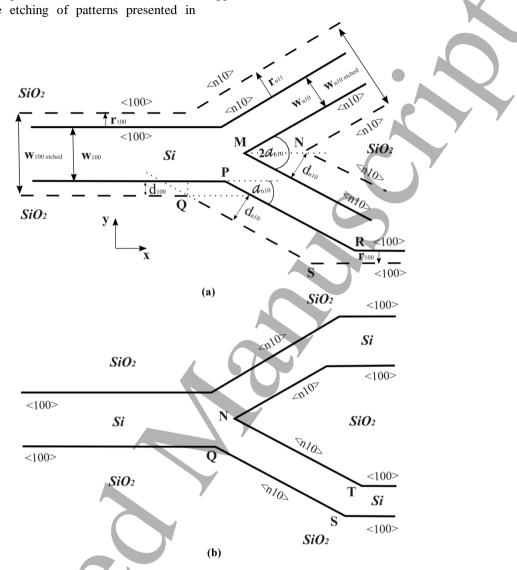


Figure 1. a) Schematic picture of Y-bifurcated microchannel's etching evolution. b) Schematic picture of two level microfluidic network.

This technical note presents our new results on the (100) silicon etching in 25 wt % TMAH water solution at a temperature of 80 °C. We analysed convex corners with sides along the <n10> crystallographic directions in the masking layer where 1 < n < 8. These convex corners were stable during etching. As there the compensation that typically leaves residues is not required, our findings allow fabrication of good quality Y-bifurcated microchannels for microfluidics [29–31]. In Ref.[32], the authors fabricated bifurcated silicon channels in the <100> and <110> directions using TMAH-Triton etchant to reduce convex corner underetching. In addition to all the etched silicon microstructures without any

compensation using 25 wt % TMAH+NC-200, authors in Ref.[22] presented a Y-bifurcated microchannel. We demonstrate basic designs with sides along predetermined <n10> crystallographic directions different from the standard <110> direction etched in pure 25 wt % TMAH water solution. The presented convex corners provide possibilities for various novel designs with angles that are deflected from the standard <100> direction. The combination of presented basic patterns allows the production of complex silicon microfluidic platforms, especially networks of microchannels [33].

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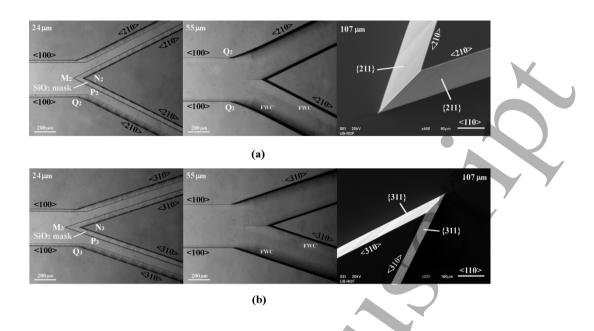


Figure 2. Photographs and SEM microphotographs of etching evolution of Y-bifurcated microchannels with the sides along crystallographic directions <100> and: a) <210>, b) <310>. The SiO₂ masking layer is present in the photographs for the etch depth of 24 μ m. SiO₂ is removed after reaching etch depths of 55 and 107 μ m. The SEM microphotographs present enlarged convex corners etched from acute angles in the masking layer.

2. Experimental setup

In our experiments [15–16,26–28], we used phosphorusdoped {100} - oriented 3" silicon wafers (SWI) with doublesided polished surfaces. The resistivity of the wafers was 1-5 Ω .cm. The wafers were cleaned in a standard procedure using a freshly prepared mixture of concentrated sulfuric acid and hydrogen peroxide, followed by RCA processing steps (mixture of ammonium hydroxide, hydrogen peroxide, and water; mixture of hydrochloric acid, hydrogen peroxide, and water). Wet thermal oxidation was performed at 1100 °C to obtain a SiO₂ masking layer. We defined bifurcations with sides along the predetermined crystallographic directions in the photolithographic processes using buffered hydrofluoric acid (BHF). After photolithographic processes, the wafers were again cleaned using the standard procedure. Native SiO₂ was removed as the wafers were dipped for 30 s in hydrofluoric acid HF (10%) and then rinsed in deionised water. Anisotropic etching was performed in pure TMAH 25 wt. % water solution (Merck) at the temperature of 80 °C.

Wafers were etched in a thermostated glass vessel closed with a Teflon lid. The vessel was on the top of a hot plate where the TMAH solution temperature was stabilised within ± 0.5 °C by the electronic temperature controller. The evaporation during etching was minimised using a Teflon lid that included a water-cooled condenser. The glass vessel contained approximately 0.8 dm³ of the solution. The velocity of the electromagnetically stirred TMAH solution was 300 rpm. We oriented the wafers vertically in a Teflon basket during etching. At the end of the silicon etching, the masking layer was removed using the BHF.

3. Results and discussion

Bifurcated microchannels are designed with sides along the predetermined crystallographic directions $\langle n10 \rangle$ (1 $\langle n \langle 8 \rangle$) and <100>, as shown in Figures 1-4. Bifurcations consist of two obtuse and one acute angle in the masking layer, with the values mentioned in Table 1. The obtuse angles in the masking layer (angle P in Figure 1(a), angles Pn, n>1 in Figures 2-4) were formed by the <n10> and <100> crystallographic directions, as in Ref.[28]. In the first case of n=2, the sidewalls are defined by $\{211\} - \{311\}$ families along the initial $\langle 210 \rangle$ crystallographic direction (Figure 2a), as in the case of an etched square with sides along the <210> crystallographic directions presented in Ref. [26,28]. The transition from {211} to {311} planes is smooth. In addition, a smooth distortion of the obtuse angle Q₂ is observed because it is defined by the {100} and {311} planes. The sidewalls of the etched obtuse angle in all other cases (n>2) are defined only by the $\{n11\}$ and {100} families, as shown in Figures 2(b),3,4. The sidewalls aligned with the <n10> direction belong to the {n11} crystallographic planes, as in Ref.[28]. The angles γ_{n11} between the $\{n11\}$ and $\{100\}$ planes are given in Table 2. In the cases where n>2, the sidewalls developed at the obtuse angles Q_n sustain as the etching continues [28].

Page 4 of 8

The acute angles (angle M in Figure 1(a)) of the presented bifurcations are formed in symmetrical $\langle n10 \rangle$ crystallographic directions with respect to the predetermined $\langle 100 \rangle$ crystallographic direction. Angles α_{n10} between the appropriate $\langle n10 \rangle$ and $\langle 100 \rangle$ crystallographic directions are smaller than 45 °. The acute angles in the masking layer are represented by $2\alpha_{n10}$, which are provided in Table 1. The sidewalls of acute angles aligned to the $\langle n10 \rangle$ directions are from the {n11} families [26,28]. We also analysed two acute angles formed by the <n10> and <m10> crystallographic directions, where m \neq n, as shown in Figure 5. The sidewalls of these asymmetrical acute angles in Figure 5 are defined by the {311} and {411} families as well as by the {311} and {511} families. These acute angles can be used for the fabrication of bifurcations with asymmetrical microchannels. In addition, the 3D silicon shape obtained in Figure 5 can

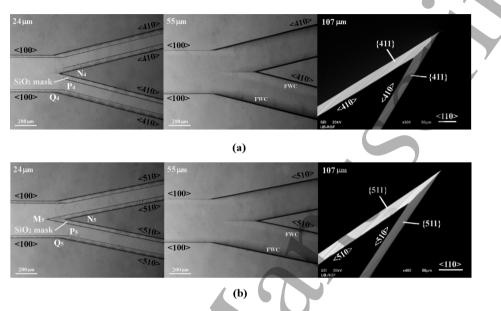


Figure 3. Photographs and SEM microphotographs of etching evolution of Y-bifurcated microchannels with sides along crystallographic directions <100> and: a) <410>, b) <510>. The SiO₂ masking layer is present in the photographs for the etch depth of 24 μ m. SiO₂ is removed after reaching etch depths of 55 and 107 μ m. The SEM microphotographs present enlarged convex corners etched from acute angles in the masking layer.

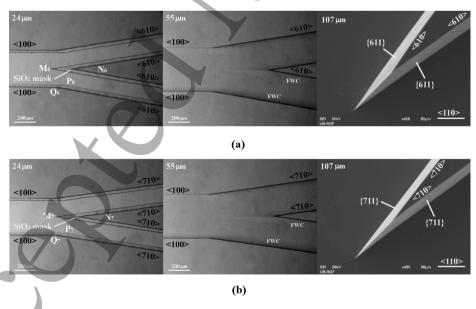


Figure 4. Photographs and SEM microphotographs of etching evolution of Y-bifurcated microchannels with sides along crystallographic directions <100> and: a) <610>, b) <710>. The SiO₂ masking layer is present in the photographs for the etch depth of 24 μ m. SiO₂ is removed after reaching etch depths of 55 and 107 μ m. The SEM microphotographs present enlarged convex corners etched from acute angles in the masking layer.

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be used as an integrated obstacle in microchannels on the platforms that are based on obstacle mechanisms such as micromixers [28]. As no new crystallographic planes appear during etching, none of the observed acute angles is distorted

Fluid flow in fabricated microchannels can be affected by the residues caused by the convex corner compensation. As no convex corner compensation is applied, there are no residues at the bottom of the etched silicon microchannels. There are facets with a weak curvature (FWC) [15] at every connection of the sidewalls with the bottom surface. The appearance of FWC is inevitable and cannot be compensated in any way. The angles of these facets cannot be determined because of the smooth transition from the bottom to the sidewalls. The appearance of the smooth FWC can influence the crosssectional shapes of the obtained microchannels.

The widths of the etched microchannels $w_{100etched}$ and $w_{n10etched}$, as presented in Figure 1(a), depend on the etch depth d_{100} :

$$w_{100etched} = w_{100} + 2d_{100} \tag{1}$$

 $w_{n10etched} = w_{n10} + 2 \frac{r_{n11}}{r_{100} sin\gamma_{n11}} d_{100} = w_{n10} + 2U_{n11} d_{100}$ (2)

 $U_{n11} = \frac{r_{n11}}{r_{100} sin\gamma_{n11}} \tag{3}$

where w_{100} and w_{n10} are the widths of the microchannel with sides along the <100> and <n10> crystallographic directions,

respectively, before etching, r_{n11} is etch rate of the {n11} crystallographic planes and U_{n11} is the undercut ratio defined only along one <n10> side of the parallelogram [28]. In Table 2, we provide the values of r_{n11} , γ_{n11} , and U_{n11} [26,28]. The value of {100} etch rate r_{100} is 0.46 µm/min [26]. As an example, we also present in Table 2 widths $w_{n10etched}$ that are calculated using formula (2) and measured in experiments for etch depth of 55 µm.

The vertices of the acute and obtuse angles in the masking layer change their position during etching, as indicated by $P \rightarrow Q$ and $M \rightarrow N$ in Figure 1. The position of the vertices depends on the values of r_{n11} and r_{100} , and should be considered during the design of bifurcations:

$$d_{n10} = U_{n11}d_{100} \tag{4}$$

$$X_{P \to Q} = \frac{d_{n10}}{d_{n10}} - \frac{d_{100}}{d_{ga_{n10}}} = d_{100} \left(\frac{d_{n11}}{d_{sina_{n10}}} - \frac{d_{100}}{d_{ga_{n10}}} \right)$$
(5)

$$I_{P \to Q} - u_{100}$$
 (0)

$$X_{M \to N} = \frac{a_{n10}}{\sin a_{n10}} = d_{100} \frac{b_{n11}}{\sin a_{n10}} \tag{7}$$

where, $X_{P \to Q}$, $Y_{P \to Q}$ and $X_{M \to N}$ are the absolute displacements of vertices in the x and y directions (<100> crystallographic directions). The derived analytical relation allows good control over the dimensions during the etching of the silicon structure in anisotropic TMAH 25 wt. % water solution.

Crystallographic direction <n10></n10>	Angle between <n10>and<100> αn10[°]</n10>	Acute angle 2α _{n10} [°]	Obtuse angle [°]	
<210>	26.6	53.2	153.4	
<310>	18.4	36.8	161.6	
<410>	14	28	166	
<510>	11.3	22.6	168.7	
<610>	9.5	19	170.5	
<710>	8.1	16.2	171.9	

Table 1. Acute and obtuse angles of microchannels.

Table 2. Angles γ_{n11} between planes {n11} and {100}, etch rates r_{n11} , ratios of undercut and etch depth U_{n11} , predetermined widths w_{n10} , calculated $w_{n10etched_c}$ and measured $w_{n10etched_m}$ widths for etched depth of 55 µm.

Crystallograhic plane {n11}	Angle Yn11theoretical ["]	Angle γ _{n11} [26] [°]	Etch rates r _{n11} [26] [μm/min]	Un11 [28]	<i>₩_{n10}</i> [μm]	Wn10etched_c [µm]	Wn10etched_m [µm]
{211}	65.9	66.7	0.87	2.06	70	297	306
{311}	72.5	74.2	0.93	2.10	70	301	307
{411}	76.4	78.7	0.85	1.89	90	298	299
{511}	78.9	80.9	0.81	1.78	100	296	288
{611}	80.7	81	0.73	1.61	120	297	288
{711}	82	83.1	0.69	1.52	130	297	286

In our previous work [26], we analysed the etching of square apertures with sides along the $\langle n10 \rangle$ crystallographic directions. In cases where n>1, the initial right concave corners changed into three new concave corners during etching. One of the angles is formed by the $\langle n10 \rangle$ and $\langle 100 \rangle$

crystallographic directions (angle R in Figure 1(a)), and the value of the concave angle in the masking layer is 180 °+ α_{n10} . The sidewalls of the concave angle are also defined by planes of {n11} and {100} families, in the same way as the etched obtuse angle of bifurcation explained above.

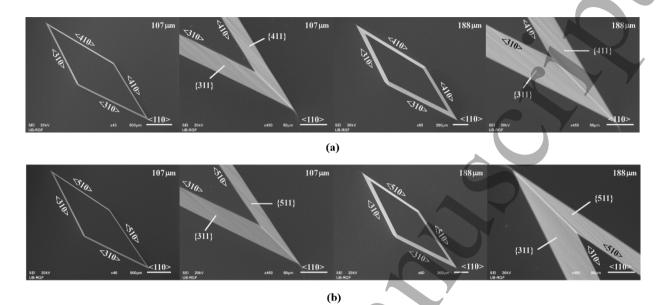


Figure 5. SEM microphotographs of etching evolution of 3D silicon structure with sides in the masking layer along crystallographic directions: a) $\langle 310 \rangle$ and $\langle 410 \rangle$, b) $\langle 310 \rangle$ and $\langle 510 \rangle$. Enlarged convex corners etched from asymmetrical acute angles in the masking layer are given. SiO₂ is removed after reaching etch depths of 107 and 188 µm.

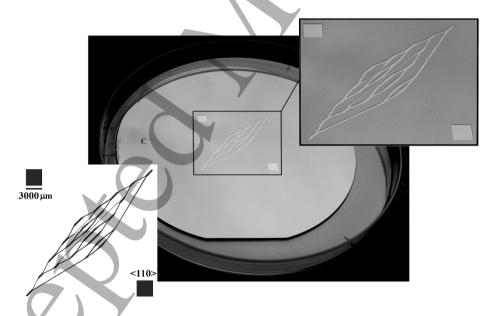


Figure 6. Mask pattern and photograph of multilevel microfluidic network of etched microchannels. The etch depth is 55 µm.

Etching of various convex corners without compensation presented in this study, together with undistorted convex and concave angles from our previous work [26,28] enables the fabrication of multilevel microfluidic networks, as shown in Figure 1(b),6. Figure 6 shows etched bifurcations with different angles and widths of microchannels that present an example of a complex microfluidic network. The combination of microchannels with sides along <n10> and <100>

crystallographic directions has a significant practical impact because of its simple design and fabrication. In addition, wet etching of silicon in 25 wt % TMAH water solution is a costeffective process in comparison with dry etching commonly used for various microfluidic platforms [34-37]. The doublesided polished wafer enables double-sided etching to form microchannels on the top side. It also provides inlets and outlets for fluid flow at the bottom side. In this case, micromachining of Pyrex glass is not necessary. The doubleside etched wafers anodically bonded to Pyrex glasses on the silicon top sides are more rigid microfluidic platforms than those obtained from polydimethylsiloxane (PDMS) [38-39]. The anodic bonding of Pyrex glass and silicon wafers provides good sealing. In addition, there is no gas permeability as in the case of the most common platforms based on PDMS [40]. Predictable evolution of explored convex and concave corners in this study can be used in future fabrication of other silicon microdevices and platforms. The proposed technique allows the fabrication of predetermined convex corners for microfluidic applications. However, it cannot provide a method to fabricate circular or other irregular geometries of the microchannels.

4. Conclusion

In this technical note, the silicon etching of Y-bifurcated microchannels using 25 wt % TMAH water solution at the temperature of 80 °C has been explored. The sides of the microchannels were designed along the <n10> and <100> crystallographic directions. We demonstrated basic designs of bifurcations with sides along crystallographic directions that are different from the commonly used <110> direction. We obtained convex corners of silicon microfluidic structures that are not distorted during etching, and consequently, no convex corner compensation was applied. The sidewalls of the microchannels are defined by etched planes of the $\{n11\}$ and {100} families. Analytical relations for the position of angle vertices are determined in order to obtain controllable shapes of microchannels. The presented simple mask designs can be used to fabricate complex microfluidic networks by a costeffective process with good control over microchannel dimensions.

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Author et al

60

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